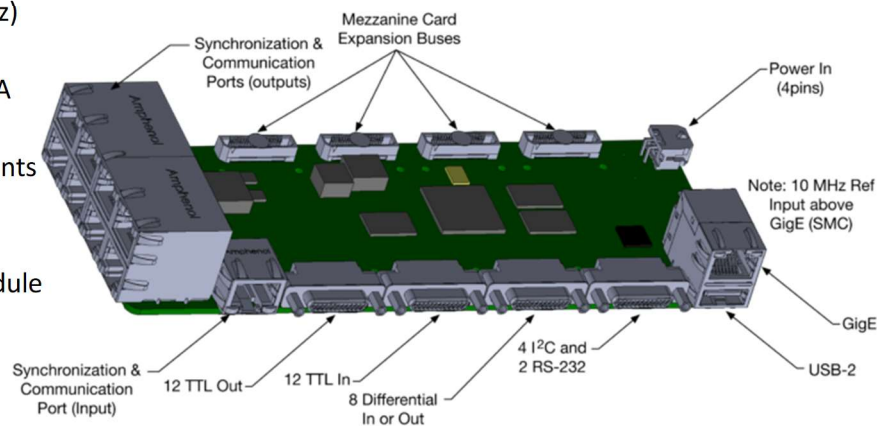


Features

- Reconfigurable at the hardware, firmware & software levels and on a pulse-to-pulse basis
- Timing signal programming simplified through XML objects including validation and verification
- Four standard interfaces providing TTL and differential I/O signals, I²C interfaces and RS-232 interfaces
- Auto discovery plugin mezzanine cards providing expanded features (e.g. GPS, ARINC 429, etc.) and custom interfaces
- Single-core Arm Cortex-A9 processor (667MHz) with application framework for run-time deployment of user applications within ARENA
- Real-time clock utilizing PPS signal
- Seamless integration into network environments
- Local storage with data rates up to 70 MB/s
- Supports embedded applications
- Modular and scalable solution with multi-module synchronization
- Ultra-low SWaP & conductive cooled
- Cost effective solution

Interfaces

- 1 Gigabit Ethernet (GigE) Network Interface (RJ-45)
- USB-2 Host Interface
- 10 MHz 50Ω coaxial connector for reference clock
- ARENA Intra-Module Synchronization Interfaces (one input and eight outputs)
- 12 TTL Single Ended Outputs (Micro-D)
- 12 TTL Single Ended Inputs (Micro-D)
- 8 Differential Input / Output – configurable (Micro-D)
- 4 I²C and 2 RS-232 Interfaces (Micro-D)



Power / Mechanical

- Power: 7-32 VDC (6-20W)
- Size: 3.0in x 7.6in x 1.35in
- Weight: 16 oz
- Conduction cooled operations from -10°C to 50°C and up to 75kft altitude
- Vertical or horizontal mounting

Mezzanine Interfaces

- Supports up to four single slot mezzanine (plugin) card or two dual slot mezzanine (plugin) cards
- Plugin cards supported:
 - Digital and analog I/O
 - I²C sensor boards
 - General data acquisition
 - GPS receiver
 - IMU / INS interfaces
 - Rapid development of custom cards

