

# Complex Digital Receiver Processors



Item	ADC <sup>1</sup>	Number of Bits	ADC Sampling Rate		Input Signal Frequency		Nyquist Zones <sup>2</sup>	Max Input Voltage	NSD (1 <sup>st</sup> Nyquist Zone) <sup>3</sup>	Interface
			Min (MSPS)	Max (MSPS)	Min (MHz)	Max (MHz)		Vpp	dBFS/Hz	
ASM-00119-00 <i>Dual Channel 2 GHz Digital Receiver</i>	AD9680-1250	14	300	1200	10	2000	1 thru 4	1.58	-151.5	JESD204
ASM-00124-00 <i>Dual Channel 1.2 GHz Digital Receiver</i>	ADS5409	12	100	900	1	1200	1 thru 3	1.0	-147.5	LVDS DDR
ASM-00121-00 <i>Quad Channel DC-Coupled 1 GHz Digital Receiver</i>	AD9684	14	250	500	DC	1000	1 thru 4	2.06	-153	LVDS DDR
ASM-00121-10 <i>Quad Channel 2GHz Digital Receiver</i>	AD9684	14	250	500	1	2000	1 thru 8	2.06	-153	LVDS DDR
ASM-00116-00 <i>Dual Channel 250 MHz Digital Receiver</i>	ADS42LB69	16	10	250	1	250	1 thru 2	2.5	-156.5	LVDS DDR
						400	1 thru 3	2.0	-154.7	
ASM-00117-00 <i>Dual Channel DC Coupled 160 MHz Digital Receiver</i>	AD9269	16	10	80	DC	160	1 thru 4	2.0	-153.2	Parallel
ASM-00125-00 <i>Dual Channel 7.5 GHz Digital Receiver</i>	AD9208	14	2500	3100	10	7500	1 thru 5	2.04	-153.7	JESD204
ASM-00126-00 <i>Single/Dual Channel 2.8 GHz Digital Receiver</i>	ADC12D1800	12	800	3600	10	2800	1, 2	0.8	-150.2	LVDS Parallel
ASM-00127-00 <i>Single/Dual Channel 8 GHz Digital Receiver</i>	ADC12DJ3200	12	800	6400	10	8000	1 thru 3	1.04	-151.4	JESD204

<sup>1</sup> Clock circuitry includes a clock divider circuit allowing for higher input clock.

<sup>2</sup> Nyquist zone for maximum sampling rate

<sup>3</sup> In higher Nyquist zones, internal ADC aperture jitter may increase noise level. See ADC specifications.



Remote Sensing Solutions, Inc.  
3179 Main Street  
Barnstable, MA 02630  
(508) 362-9400  
[www.remotesensingsolutions.com](http://www.remotesensingsolutions.com)

